

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re the Application of:)
)
Thomas J. Pennaz) Examiner:
)
Serial No.: To Be Assigned Herewith) Group Art Unit:
)
Filed: Herewith)
)
Patent: Circuit Chip Connector And)
Method Of Connecting A)
Circuit Chip)

The Honorable Commissioner of
Patents and Trademarks
Washington, D.C. 20231-9998

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

In connection with the subject application, and in accordance with the provisions of 37 C.F.R. §§1.97-1.99, submitted herewith is copy of form PTO-1449, and copies of the references cited therein.

Respectfully submitted,

March 20, 2000
Date

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UTILITY PATENT APPLICATION WITH FEE;
INVENTOR'S DECLARATION AND POWER OF ATTORNEY;
INFORMATION DISCLOSURE STATEMENT
WITH PTO FORM 1449 AND CITED REFERENCES;
ASSIGNMENT WITH COVER SHEET AND FEE

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Date of deposit: March 21, 2000

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Richard Sensenbrenner

(Typed or printed name of person mailing paper or fee)



(Signature of person mailing paper or fee)